AUS920010887US1 1/7

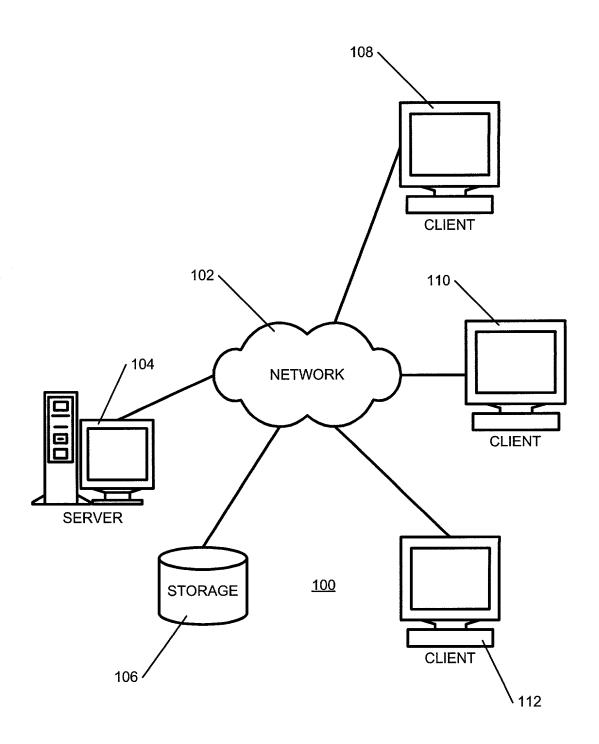
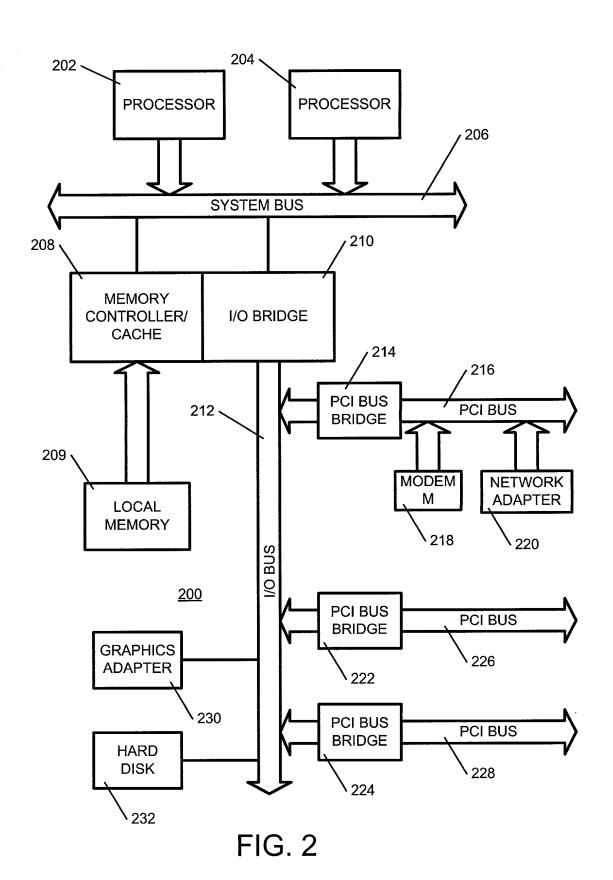
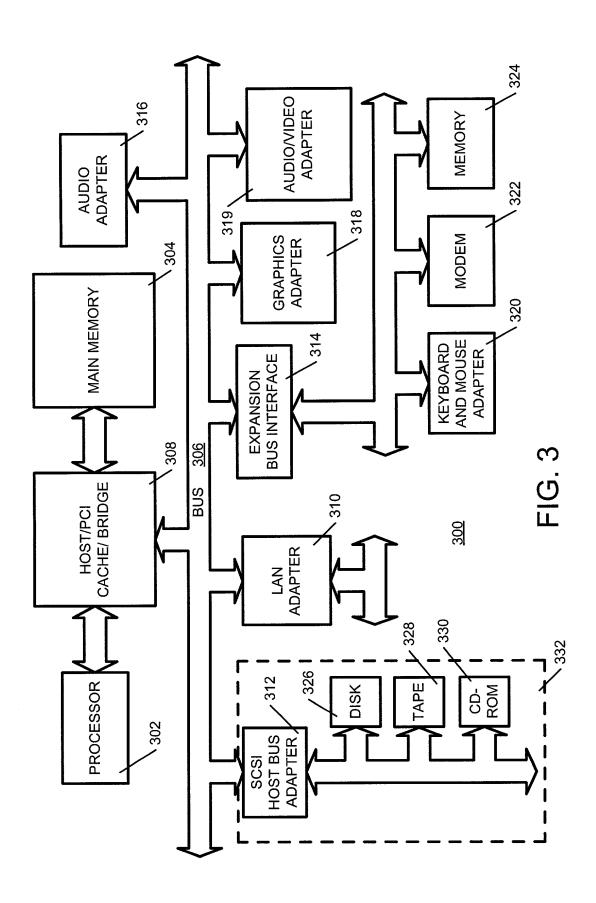


FIG. 1





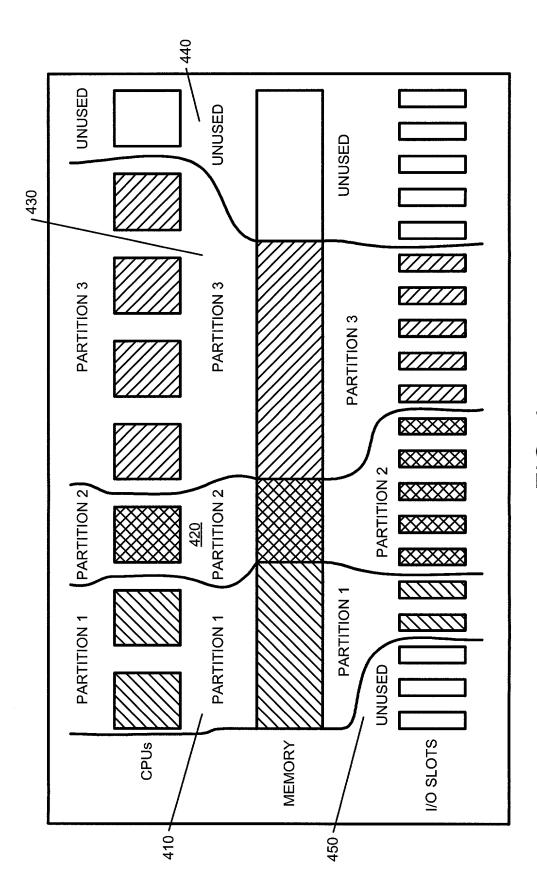


FIG. 4

	CPU'S	MEMORY LOCATIONS	I/O SLOTS
DOU PARTITION	CPU₁ & CPU₂	M ₁ - M ₅₀	SLOT4 & SLOT5
502 —— PARTITION ₂	CPU_3	M ₅₁ - M ₇₅	SLOT ₆ - SLOT ₁₀
504 PARTITION ₃	CPU₄ - CPU ₇	M ₇₆ - M ₁₅₀	SLOT ₁₁ - SLOT ₁₅

FIG. 5

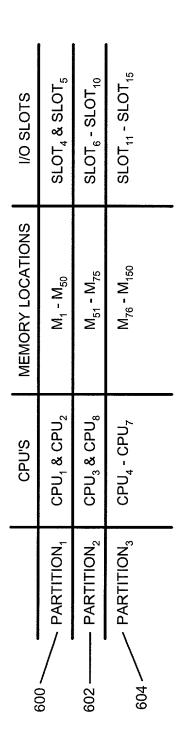


FIG. 6

